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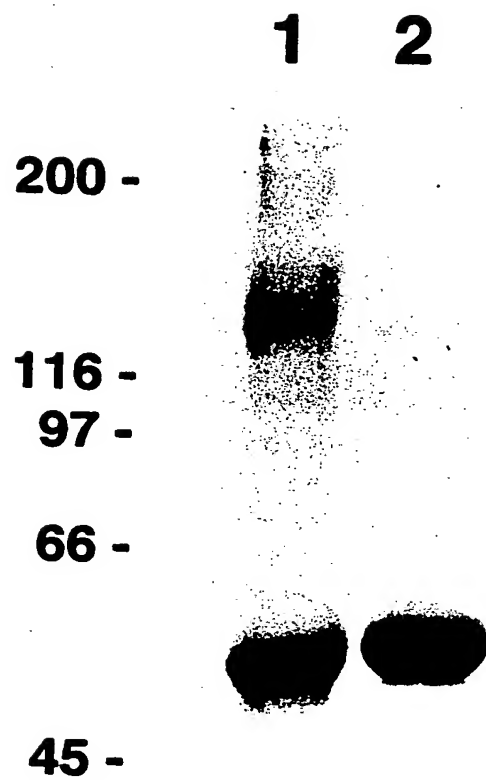


Fig. 1

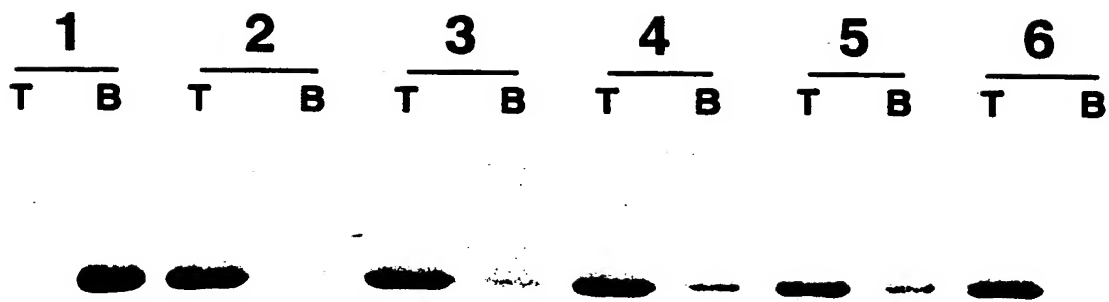


Fig. 2

P **19 S** **11.3 S** **4.4 S**
 ▼ ▼ ▼

Fig. 3A

Fig. 3B

160 S 70 S 19 S
 ↓ ↓ ↓
 P 1 2 3 4 5 6 7 8 9 10 11 12

Fig. 4A



Fig. 4B



Fig. 4C



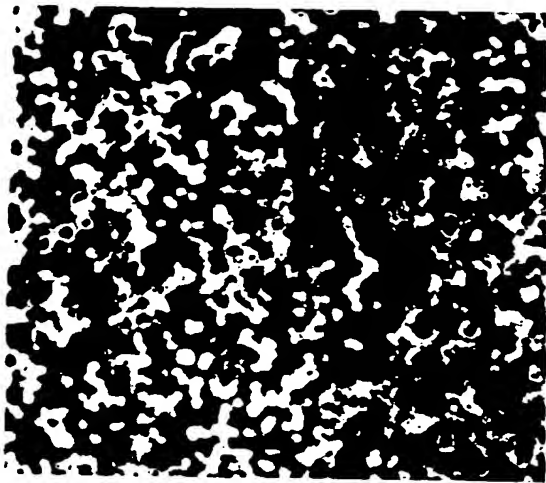


Fig. 5A

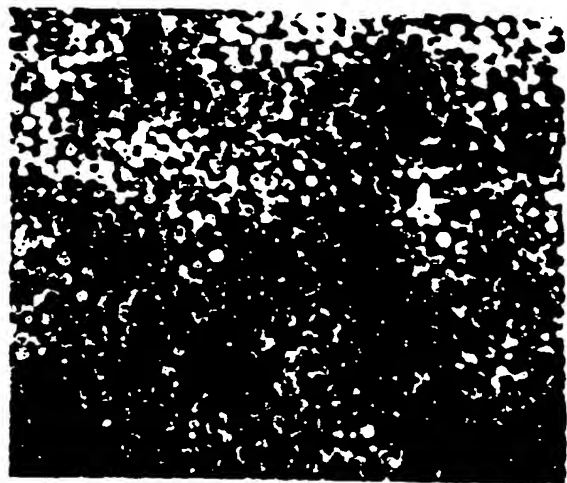


Fig. 5C

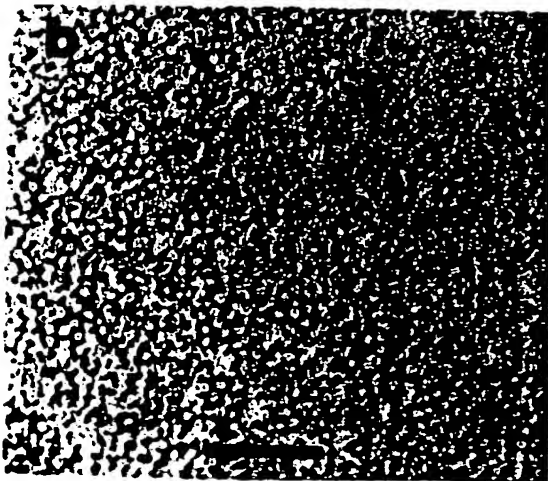


Fig. 5B

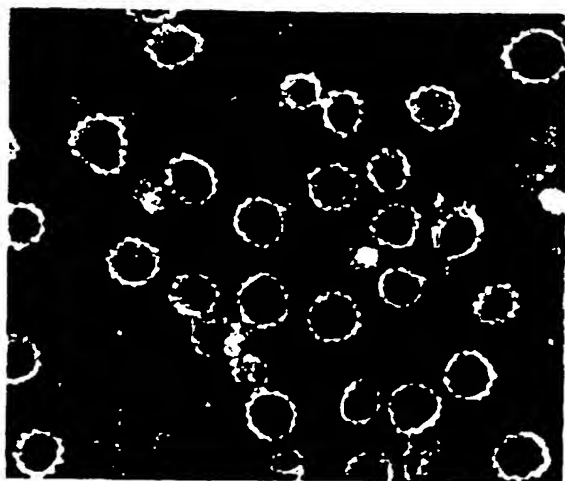


Fig. 5D

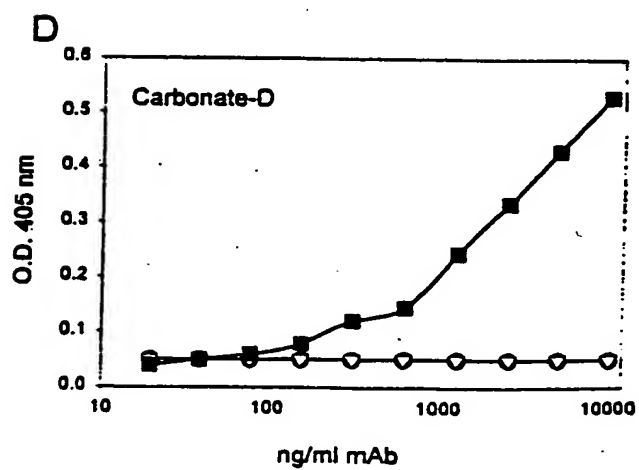
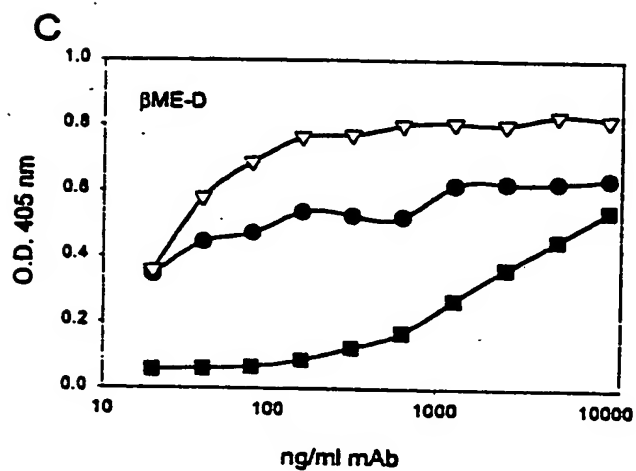
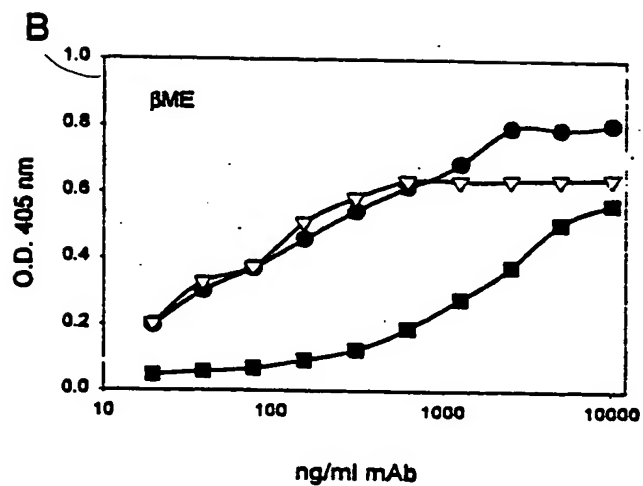
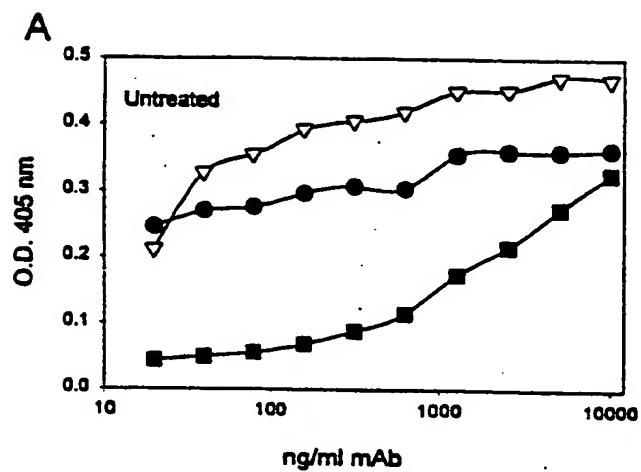


FIGURE 6

			<u>Starting VLPs</u>					<u>Reassembled VLPs</u>				
S	C	V	-3	-4	-5	-6	-7	-3	-4	-5	-6	-7

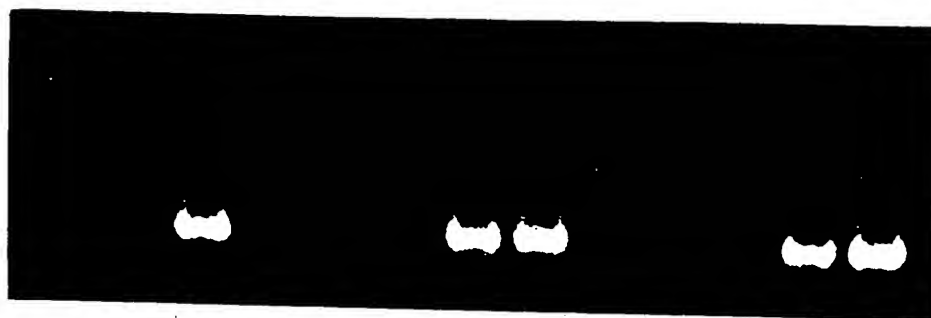


Fig. 7A

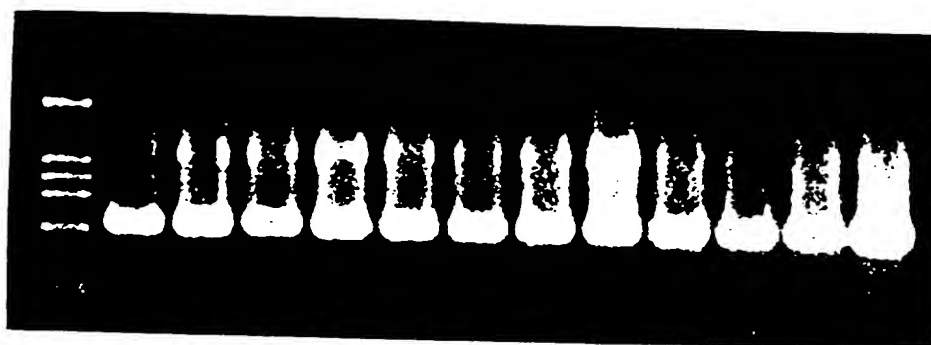


Fig. 7B

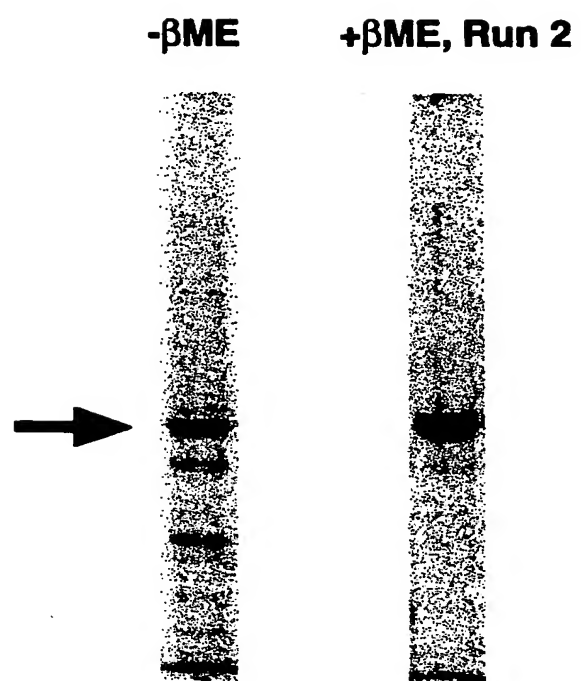


Fig. 8

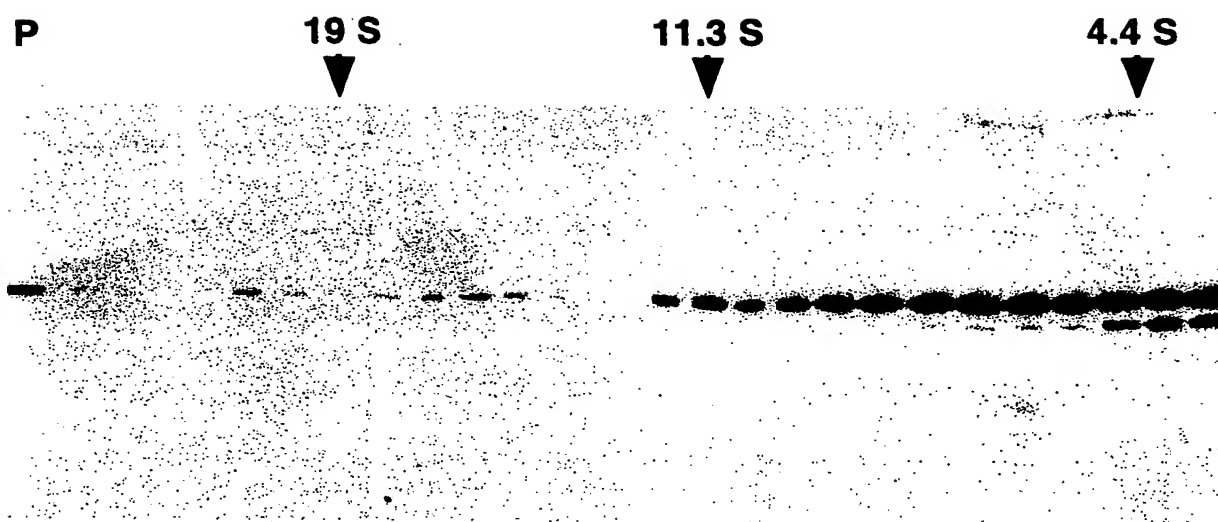


Fig. 9

P 160 S 70 S 19 S



Fig. 10A

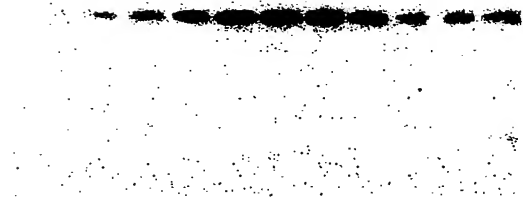


Fig. 10B

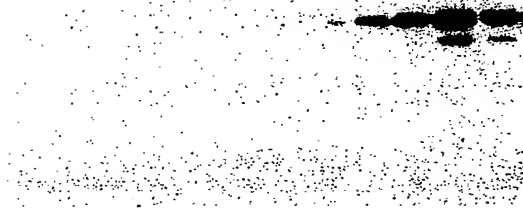


Fig. 10C

